Product data sheet

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Extended temperature range T_i = 175 °C
- Trench MOSFET technology
- Very fast switching
- AEC-Q101 qualified

3. Applications

- Relay driver
- · High-speed line driver
- · High-side load switch
- · Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-20	V
V _{GS}	gate-source voltage			-8	-	8	V
I _D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-	-2.6	Α
Static characte	eristics						
R _{DSon}	drain-source on-state resistance	$V_{GS} = -4.5 \text{ V}; I_D = -2.6 \text{ A}; T_j = 25 \text{ °C}$		-	97	118	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain		G P
				Š
			SOT23	017aaa094

6. Ordering information

Table 3. Ordering information

table of ordering information						
Type number Package						
	Name	Description	Version			
BSH205G2A		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BSH205G2A	Q8%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-20	V
V _{GS}	gate-source voltage			-8	8	V
I _D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-2.6	Α
		V _{GS} = -4.5 V; T _{amb} = 100 °C	[1]	-	-1.6	Α
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	-10	Α
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	610	mW
			[1]	-	1.3	W
		T _{sp} = 25 °C		-	10	W
Tj	junction temperature			-55	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C
Source-drain	n diode					
Is	source current	T _{amb} = 25 °C	[1]	-	-1.2	Α
Avalanche r	uggedness	,		'		,
E _{DS(AL)S}	non-repetitive drain- source avalanche energy	T _{j(init)} = 25 °C; I _D = -0.5 A; DUT in avalanche (unclamped)		-	5	mJ

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

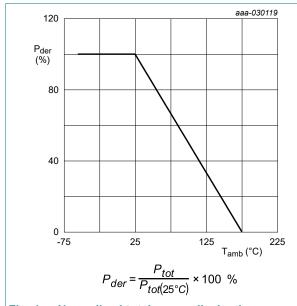


Fig. 1. Normalized total power dissipation as a function of ambient temperature

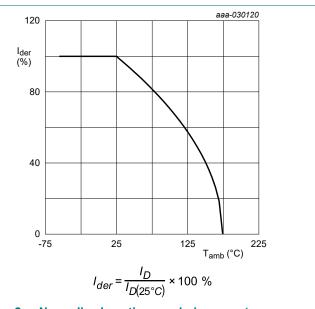


Fig. 2. Normalized continuous drain current as a function of ambient temperature

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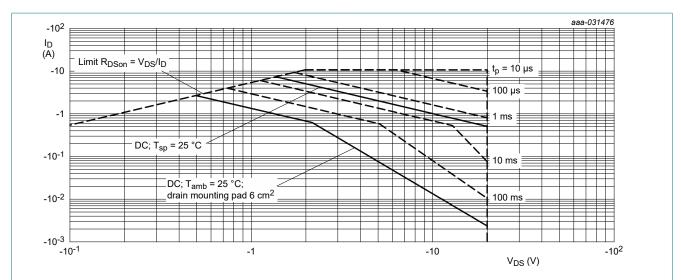


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1]	-	218	250	K/W
junction to ar	junction to ambient		[2]	-	97	114	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	10	15	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm².

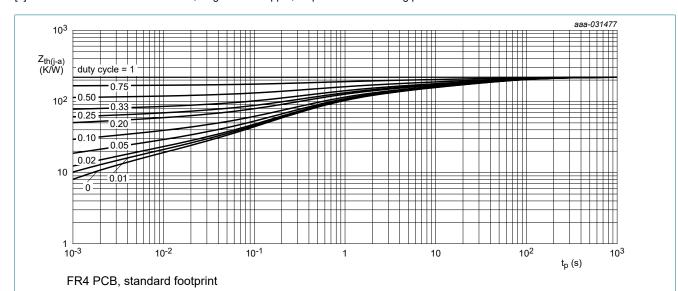


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

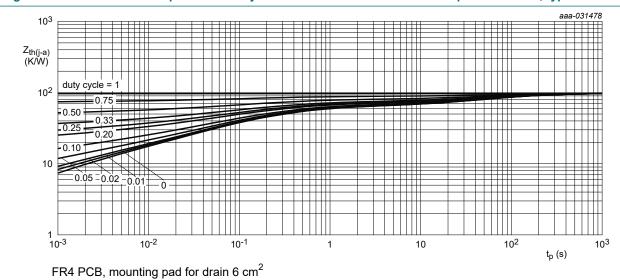


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V _{(BR)DSS}	drain-source breakdown voltage	$I_D = -250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	-20	-	-	V
V_{GSth}	gate-source threshold voltage	I_D = -250 μ A; V_{DS} = V_{GS} ; T_j = 25 °C	-0.4	-0.65	-0.9	V
I _{DSS}	drain leakage current	$V_{DS} = -20 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	-1	μΑ
I _{GSS}	gate leakage current	V _{GS} = -8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-100	nA
		V _{GS} = 8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	100	nA
R _{DSon}	drain-source on-state	$V_{GS} = -4.5 \text{ V}; I_D = -2.6 \text{ A}; T_j = 25 \text{ °C}$	-	97	118	mΩ
	resistance	$V_{GS} = -4.5 \text{ V}; I_D = -2.6 \text{ A}; T_j = 175 \text{ °C}$	-	153	186	mΩ
		$V_{GS} = -2.5 \text{ V}; I_D = -2.3 \text{ A}; T_j = 25 \text{ °C}$	-	118	152	mΩ
		V _{GS} = -1.8 V; I _D = -0.5 A; T _j = 25 °C	-	145	240	mΩ
		$V_{GS} = -1.5 \text{ V}; I_D = -0.1 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	170	400	mΩ
g _{fs}	forward transconductance	$V_{DS} = -10 \text{ V}; I_D = -2.6 \text{ A}; T_j = 25 \text{ °C}$	-	5	-	S
R_{G}	gate resistance	f = 1 MHz	-	8.3	-	Ω
Dynamic ch	naracteristics					
Q _{G(tot)}	total gate charge	V_{DS} = -10 V; I_D = -2.6 A; V_{GS} = -4.5 V;	-	4.6	7	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.6	-	nC
Q_{GD}	gate-drain charge	1	-	1.2	-	nC
C _{iss}	input capacitance	V _{DS} = -10 V; f = 1 MHz; V _{GS} = 0 V;	-	421	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	38	-	pF
C _{rss}	reverse transfer capacitance		-	32	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -10 V; I_D = -2.6 A; V_{GS} = -4.5 V;	-	5	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	13	-	ns
t _{d(off)}	turn-off delay time	1	-	35	-	ns
t _f	fall time	1	-	12	-	ns
Source-dra	in diode		'		,	
V _{SD}	source-drain voltage	$I_S = -1.2 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	-0.8	-1.2	V
t _{rr}	reverse recovery time	I _S = -1.1 A; dI _S /dt = 100 A/μs;	-	7	-	ns
Q _r	recovered charge	$V_{GS} = 0 \text{ V}; V_{DS} = -10 \text{ V}; T_j = 25 \text{ °C}$	-	1	-	nC

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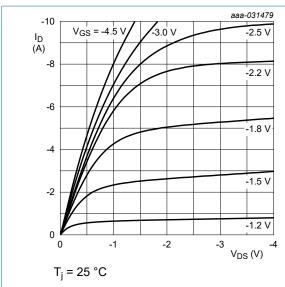


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

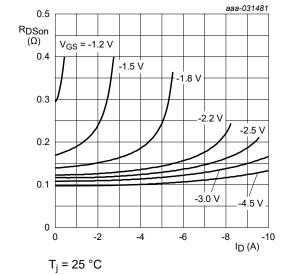


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

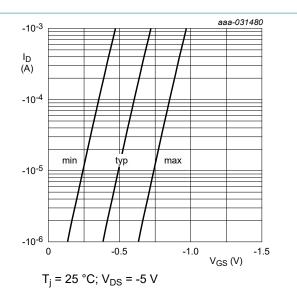


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

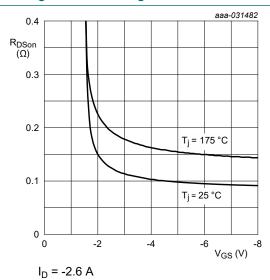


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

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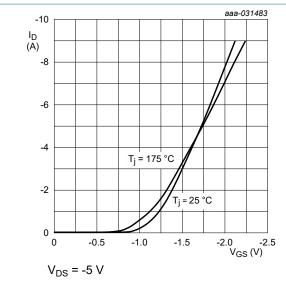


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

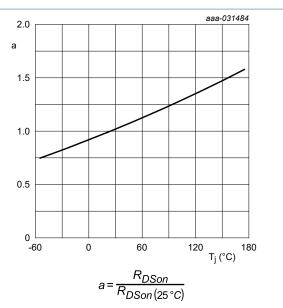


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

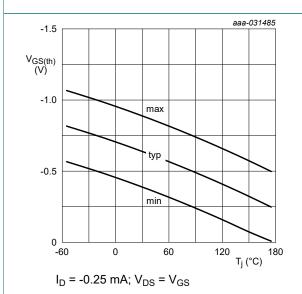


Fig. 12. Gate-source threshold voltage as a function of junction temperature

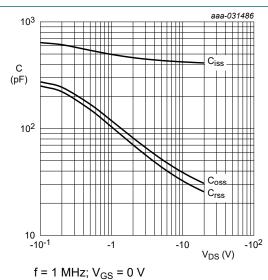


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

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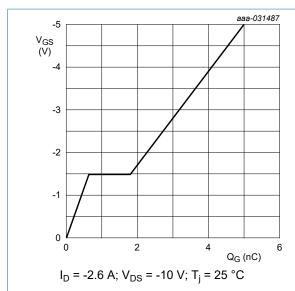


Fig. 14. Gate-source voltage as a function of gate charge; typical values

 $V_{GS} = 0 V$

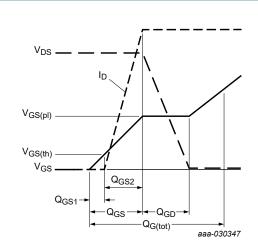


Fig. 15. Gate charge waveform definitions

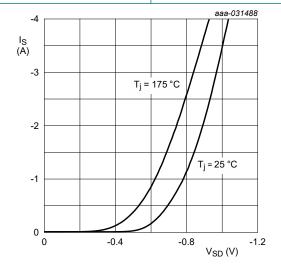
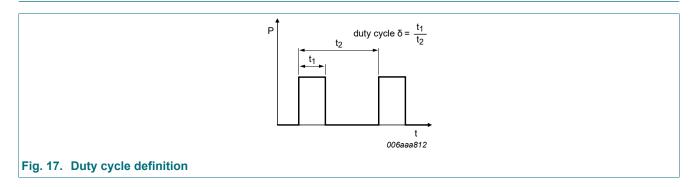


Fig. 16. Source current as a function of source-drain voltage; typical values

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11. Test information



Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline

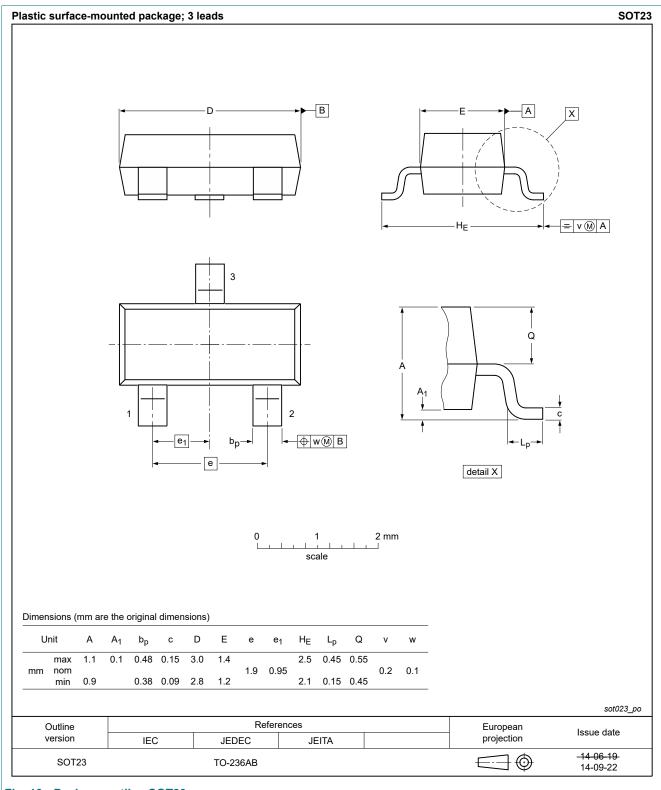
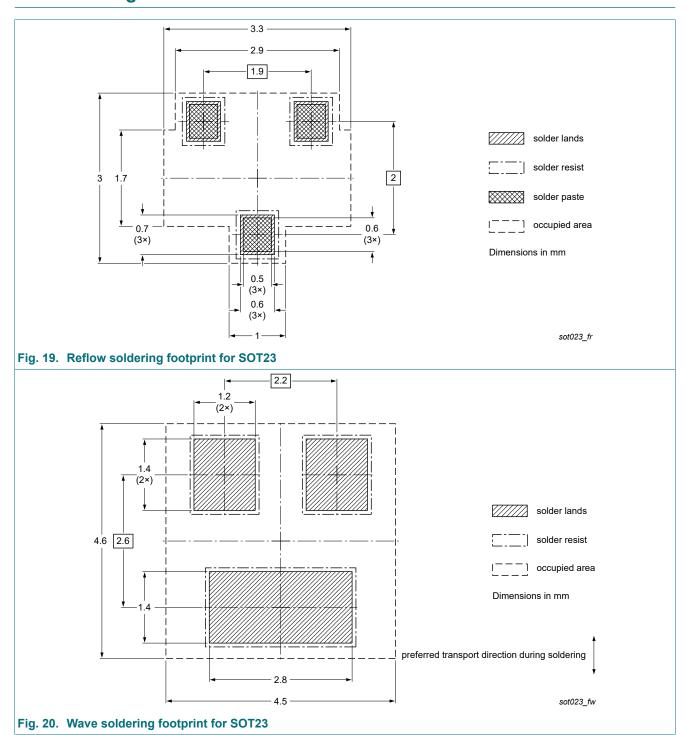


Fig. 18. Package outline SOT23

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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BSH205G2A v.1	20201013	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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BSH205G2A

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